AUS920010887US1 1/7

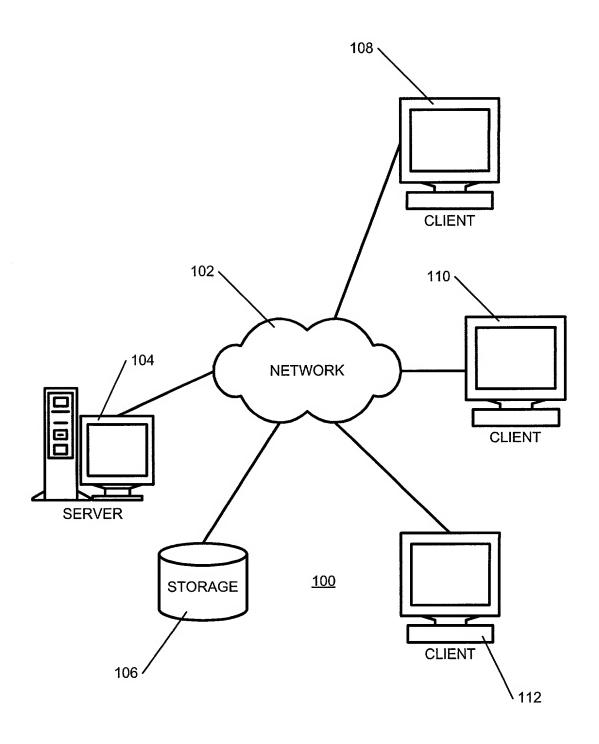
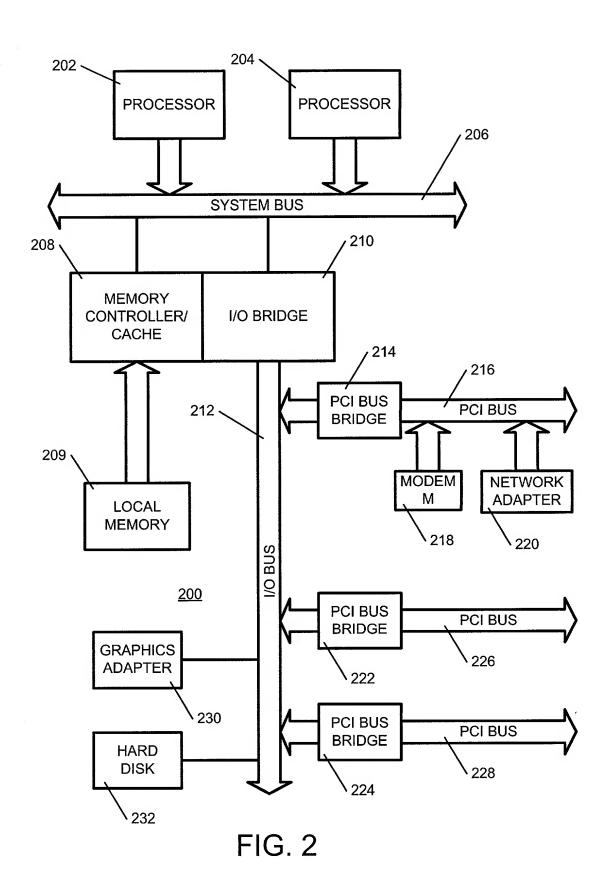
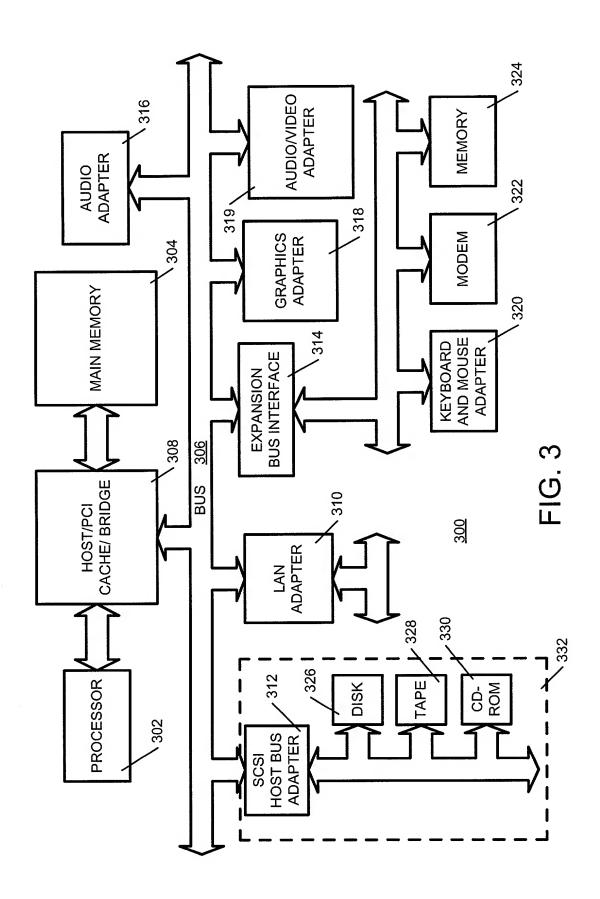


FIG. 1





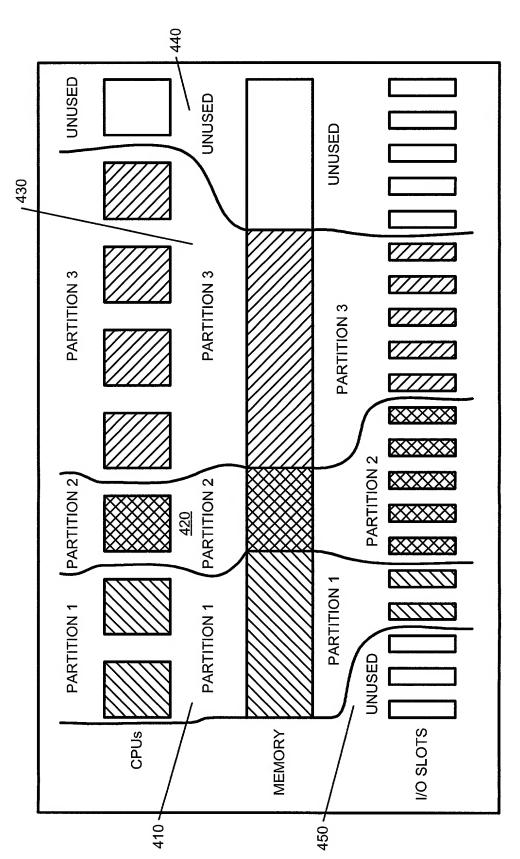


FIG. 4

I/O SLOTS	SLOT ₄ & SLOT ₅	SLOT ₆ - SLOT ₁₀	SLOT ₁₁ - SLOT ₁₅
MEMORY LOCATIONS	M ₁ - M ₅₀	M ₅₁ - M ₇₅	M ₇₆ - M ₁₅₀
CPU'S	CPU₁ & CPU₂	CPU3	CPU₄ - CPU ₇
CCL	SUU PARTITION,	502 ——— PARTITION ₂	504 PARTITION ₃

FIG. 5

I/O SLOTS	SLOT₄ & SLOT₅	SLOT ₆ - SLOT ₁₀	SLOT ₁₁ - SLOT ₁₅
MEMORY LOCATIONS	M ₁ - M ₅₀	M ₅₁ - M ₇₅	M ₇₆ - M ₁₅₀
CPU'S	CPU₁ & CPU₂	CPU ₃ & CPU ₈	CPU₄ - CPU ₇
	PARTITION,	602 —— PARTITION ₂	604 PARTITION ₃

FIG. 6

